# UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,193,464 B2

Page 1 of 1

APPLICATION NO.: 10/015887

DATED INVENTOR(S) : March 20, 2007 : Wang et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

## Column 14

Line 45 please replace "vovMS" with --vovM8--.

#### Column 16 Claim 2

Line 63 please replace "pairs" with --pair--.

#### Column 17 Claim 7

Line 23 please replace "step" with --steps--.

## Column 18 Claim 7

Line 3 please replace "offset voltage" with --level shifted voltage--.

# Column 18 Claim 9

Line 9 please replace "step" with --steps--.

Signed and Sealed this

Tenth Day of July, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office